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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/816,045	04/01/2004	Venkat Selvamánickam	1014-SP127	9625

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LARSON NEWMAN ABEL POLANSKY & WHITE, LLP
5914 WEST COURTYARD DRIVE
SUITE 200
AUSTIN, TX 78730

EXAMINER

TALBOT, BRIAN K

ART UNIT	PAPER NUMBER
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1762

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	01/25/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary

Application No.

10/816,045

Applicant(s)

SELVAMANICKAM ET AL.

Examiner

Brian K. Talbot

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 27 October 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-33,39 and 40 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-33,39 and 40 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

1. The amendment filed 10/27/06 has been considered and entered. Claims 34-38 have been canceled. Claims 39 and 40 have been added. Claims 1-33 and 39-40 remain in the application.
2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
3. In light of the amendment filed 10/27/06, the 35 USC 102 rejection over Ignatiev et al. (2004/0016401) has been withdrawn. The 35 USC 103 rejection under 35 U.S.C. 103(a) as being unpatentable over R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) has also been withdrawn. However, the following rejection has been necessitated by the amendment.

Claim Rejections - 35 USC § 112

4. Claim 8 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

With respect to claim 8, the claim is not further limiting. The dependent claims (7 and 1) require that the substrate is translated continuously (i.e. reel-to-reel) while be subjected to mechanical waves in the fluid (claim 1). Clarification is requested.

Claim Rejections - 35 USC § 103

5. Claims 1-14,22-24,27-33,39 and 40 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) further in combination with JP 02-118075 and JP 02-118,061.

Ignatiev et al. (2004/0016401) teaches a method and apparatus for forming a superconductor material on a tape substrate. The invention uses a pay-out reel and take-up reel to dispense and spool the tape. The process utilizes a reaction chamber for cleaning the substrate, applying buffer layers and applying superconductor layers (abstract). The tape (408) should be cleaned and free of grease and other contaminates. A vapor degreaser or cleaner can be used in the pre-clean stage. Alternatively a mechanical cleaner or ultrasonic bath can be utilized. Initialization stage pre-heats and or pre-treats the tape (408) (col. 3, [0031] – [0033]).

R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" teaches a continuous reel-to-reel deposition of oriented buffer layers and RBCO-superconductor. In a first step, a high temperature steel tape is electropolished and cleaned at a speed of 5-10 m/h to remove defects and to provide a surface finish (rms of less than 1.5 nm). Subsequently a MgO buffer layer is grown at room temperature using ISD and then a superconductor layer of RBCO is applied epitaxially. A final sputtering step of applying a gold/silver layer to the superconductor layer is disclosed. The T_c is 77K.

R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" fails to teach the claimed pretreatment of "mechanical waves".

Ignatiev et al. (2004/0016401) teaches cleaning a substrate prior to applying buffer and superconductive layers including an ultrasonic bath.

Therefore it would have been obvious at the time the invention was made to have modified R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" by substituting one well known precleaning treatment (ultrasonic waves) for another (electropolishing) as evidenced by Ignatiev et al. (2004/0016401) with the expectation of achieving similar success.

Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) fail to teach utilizing a plasma pretreatment as well.

JP 02-118075 and JP 02-118,061 both teach advantages associated with plasma cleaning a substrate (in this case another superconductive layer) prior to forming a second superconductive layer to remove contaminants and improve adhesiveness between the layers (abstract).

Therefore it would have been obvious at the time the invention was made to have modified Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) process by incorporating a plasma pretreatment as evidenced by JP 02-118075 and JP 02-118,061 with the expectation of improving the adhesiveness between the substrate and the superconductive coating.

With respect to claims 2-6,9 and 10 the claims recite sound waves, water medium, frequency of the waves and reel speed. It is the Examiner's position that these features are all

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“result effective” variables which are optimized through routine experimentation of a practitioner in the art. Absent a showing of unexpected results regarding these features, it is the Examiner’s that it would have been obvious for one skilled in the art to have selected the claimed features with the expectation of achieving similar success.

With respect to claims 11-13, the claims recite a polishing step. The art teaches cleaning and polishing as viable pretreatment steps and one skilled in the art would have had a reasonable expectation of achieving an additive effect by utilizing more than one conventional precleaning step to produce a “clean” substrate.

6. Claims 25 and 26 are rejected under 35 U.S.C. 103(a) as being unpatentable Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) further in combination with JP 02-118075 and JP 02-118,061 still further in combination with Goyal et al., "Processing of High Temperature Superconductors".

Features described above concerning Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) further in combination with JP 02-118075 and JP 02-118,061 are incorporated here.

Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) further in combination with JP 02-118075 and JP 02-118,061 fail to teach a biaxially textured buffer layer of a superconductor layer being applied by IBAD.

Goyal et al., "Processing of High Temperature Superconductors" teaches a biaxially textured buffer layer of a superconductor layer being applied by IBAD deposited on polished steel substrates (abstract).

Therefore, it would have been obvious at the time the invention was made for one skilled in the art to have modified Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) further in combination with JP 02-118075 and JP 02-118,061 process by forming a biaxially textured buffer layer of a superconductor layer being applied by IBAD as evidenced by Goyal et al., "Processing of High Temperature Superconductors" with the expectation of achieving similar success.

7. Claims 15-21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) further in combination with JP 02-118075 and JP 02-118,061 further in combination with Glowacki et al. (6,251,834).

Features described above concerning Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) further in combination with JP 02-118075 and JP 02-118,061 are incorporated here.

Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401)

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further in combination with JP 02-118075 and JP 02-118,06 fail to teach the claimed pretreatment step of annealing.

Glowacki et al. (6,251,834) teaches improved substrate from growing superconducting oxide materials whereby the substrate is pretreated by an annealing procedure (col. 2, line 40 – col. 3, line 30).

Therefore it would have been obvious at the time the invention was made to have modified Ignatiev et al. (2004/0016401) alone or R. Nenetschek, et al., "Continuous Coated Conductor Fabrication by Evaporation" in combination with Ignatiev et al. (2004/0016401) further in combination with JP 02-118075 and JP 02-118,061 process by substituting one well known precleaning treatment (annealing) for another (electropolishing/ultrasound waves) as evidenced by Glowacki et al. (6,251,834) with the expectation of achieving similar success.

Response to Amendment

8. Applicant's arguments with respect to claims 1-33 and 39-40 have been considered but are moot in view of the new ground(s) of rejection.

Applicant argued that the 35 USC 112 rejection over claim 8 is not proper as claim 7 requires reel-to-reel and claim 8 requires continuously.

The Examiner disagrees. The Examiner questions the how a reel-to-reel process is not continuous.

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Applicant argued that the combination of prior art fails to teach or fairly suggest using a plasma pretreatment in forming superconductive films.

JP 02-118075 and JP 02-118,061 both teach advantages associated with plasma cleaning a substrate (in this case another superconductive layer) prior to forming a second superconductive layer to remove contaminants and improve adhesiveness between the layers.

Furthermore, Applicant has argued that the step of plasma cleaning is not known in the superconductive art.

The Examiner disagrees as noted by the references to JP 02-118075 and JP 02-118,061. Even absent the references, it would be the Examiner's position that one skilled in the art at the time the invention was made would have had a reasonable expectation of similar success regardless of whether the subsequent coating was semiconductive, superconductive or another coating. Applicant has not refuted this position with evidenced only gleaned in the superconductive art which would not also be gleaned from the other coating areas, i.e. critical current, critical temperature, etc. Improved adhesion would be achieved across the various coating processes.

9. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO**


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MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian K. Talbot whose telephone number is (571) 272-1428. The examiner can normally be reached on Monday-Friday 6AM-3PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy H. Meeks can be reached on (571) 272-1423. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

 1/3/07
Brian K Talbot
Primary Examiner
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BKT